

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF THE CLAIMS:

1. (Currently Amended) A transceiver interface connectable, in use, to a cable, the interface including an input for receiving a pair of signals from said cable, and a logic circuit connected to the input having a single logic output line, wherein the single logic output line of the logic circuit has a first output state where both signals are below a predetermined level and a second output state where either or both signals exceed the predetermined level.

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2. (Original) An interface as claimed in Claim 1, wherein the logic circuit is a NOR gate.

3. (Previously Presented) An interface as claimed in Claim 1, wherein the first output state is indicative of a single ended zero state.

4. (Previously Presented) An interface as claimed in Claim 1, wherein the second output state is indicative of a non-single ended zero state.

5. (Previously Presented) A serial bus device including a transceiver interface as claimed in claim 1.

7. (Previously Presented) A device as claimed in Claim 5, wherein the bus is Universal Serial Bus.

8. (Previously Presented) A mobile radio telephone including a device as claimed in Claim 6 as dependent therefrom, wherein the application specific integrated circuit also provides mobile telephony functions.

Claim 9 (Cancelled).

10. (Previously Presented) An interface as claimed in Claim 2, wherein the first output state is indicative of a single ended zero state.

11. (Previously Presented) An interface as claimed in Claim 2, wherein the second output state is indicative of a non-single ended zero state.

12. (Previously Presented) An interface as claimed in Claim 3, wherein the second output state is indicative of a non-single ended zero state.

13. (Previously Presented) A serial bus device including a transceiver interface as claimed in claim 2.

14. (Previously Presented) A serial bus device including a transceiver interface as claimed in claim 3.

15. (Previously Presented) A serial bus device including a transceiver interface as claimed in claim 4.

16. (Previously Presented) A device as claimed in Claim 6, wherein the bus is Universal Serial Bus.

17. (Previously Presented) A mobile radio telephone including a device as claimed in Claim 7 as dependent therefrom, wherein the application specific integrated circuit also provides mobile telephony functions.

18. (New) A transceiver interface connectable, in use, to a cable, the interface including an input for receiving a pair of signals from said cable, and a logic circuit connected to the input having only a single logic output line, wherein the logic circuit has a first output state where both signals are below a predetermined level and a second output state where either or both signals exceed the predetermined level.

19. (New) As interface as claimed in Claim 18, wherein the logic circuit is a NOR gate.

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20. (New) An interface as claimed in Claim 18, wherein the first output state is indicative of a single ended zero state.

21. (New) An interface as claimed in Claim 18, wherein the second output state is indicative of a non-single ended zero state.
